

FORM PTO-1449
(Rev. 7-80)U.S. Department of Commerce
Patent and Trademark OfficeAtty. Docket No.
TESSERA 3.0-176 DIVApplication No.
Div. of 09/277,677

LIST OF PRIOR ART CITED BY APPLICANT

(Use several sheets if necessary)

Applicant
Beroz et al.

Filing Date: Herewith

Group 2841

U.S. PATENT DOCUMENTS

*EXAMINER INITIALS		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
BT	AA	5,148,265	09/1992	Khandros et al.	357	80	
	AB	5,148,266	09/1992	Khandros et al.	357	80	
	AC	5,801,446	09/1998	DiStefano et al.	257	778	
	AD	5,519,580	05/1996	Natarajan et al.	361	760	
	AE	5,400,220	03/1995	Swamy	257	774	
	AF	5,133,495	07/1982	Amgulas et al.	228	180.1	
	AG	5,590,460	01/1997	DiStefano et al.	29	830	
	AH	5,766,987	06/1998	Mitchell et al.	438	126	
	AI	3,530,229	09/1970	Burke, Jr.	174	68.5	
	AJ	5,818,697	10/1998	Armezzani et al.	361	749	
BT	AK	3,360,852	01/1968	Cochran	29	624	

FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
							YES	NO
BT	AL	WO 95/34106	12/1995	WIPO				
BT	AM	WO 97/44859	11/1997	WIPO				
	AN							
	AO							
	AP							

OTHER PRIOR ART (Including Author, Title, Date, Pertinent Pages, Etc.)

BT	AR	Andros and Hammer, "TBGA Package Technology" IEEE Transactions on Components, Packaging and Manufacturing Technology, Part B, Vol. 17, No. 4, VP 564-568.
BT	AS	Lau, John H., Ball Grid Array Technology, pp. 460-464.
BT	AT	Publication, in American National Standard ANSI/IPC-D-249, "Design Standard for Flexible Single and Double Sided Printed Boards," January 1997, p. 47.

EXAMINER

B. Talbot

DATE CONSIDERED

12/2/02

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

 1046 U.S. PTO
09/942386


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*EXAMINER INITIALS	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE	
BT	AA	5,585,162	12/1996	Schueller	428	131	
BT	AB	4,884,170	11/1989	Ohki et al.	361	414	
BT	AC	4,616,292	10/1986	Sengoku et al.	361	414	
	AD						
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OTHER PRIOR ART (Including Author, Title, Date, Pertinent Pages, Etc.)							
BT	AR	American National Standard ANSI/IPC-D-275 of September 1991 entitled Design Standard for Rigid Printed Boards and Rigid Printed Board Assemblies, pp.62-68.					
BT	AS	Design Guidelines for Surface Mount and Fine -- Pitch Technology, 2nd edition, 1996 by Vernon Solberg, pp.142-143.					
BT	AT	Fjelstad, An Engineer's Guide to Flexible Circuit Technology (ElectroChemical Publications Limited 1997; ISBN 0901150347), pp.148-149.					
EXAMINER B. Talbot				DATE CONSIDERED 12/2/02			
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	AO						
	AP						
OTHER PRIOR ART (Including Author, Title, Date, Pertinent Pages, Etc.)							
BT	AR	Standard ANSI/IPC-D-350, Revision D, July 1992.					
BT	AS	IBM Technical Disclosure Bulletin, Vol. 40, No. 6, pp.199-200 entitled "Ball Grid Array Solder Ball on Ball Grid Array Dimple Pad" (June 1997).					
	AT						
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